

Docket: CU-3455

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application: Chang Yeol Lee et al.

GRP ART UNIT: 2813

Serial No: 10/712,921

Ex.: Chen, Jack S.J.

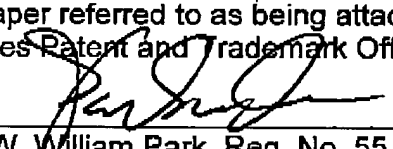
Filed: November 13, 2003

For: Method For Reducing Poly-Depletion In Dual Gate
CMOS Fabrication Process**Certification under 37 C.F.R. §1.8(a)**

The USPTO Central Fax No. (703) 872-9306

Date of Fax Transmittal: March 8, 2005

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being transmitted to the United States Patent and Trademark Office to the fax number and on the date indicated above.


W. William Park, Reg. No. 55,523

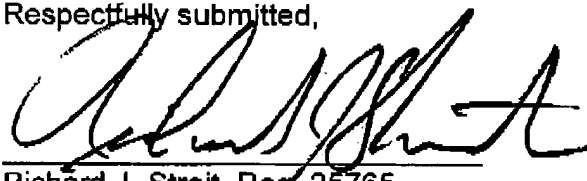
The Commissioner for Patents
P.O. Box 1450
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AUTHORIZATION TO ACT IN A REPRESENTATIVE CAPACITY

The practitioner **Woochoon William Park, Reg. No. 55,523** is authorized to conduct interviews and has the authority to bind the principal concerned. Further, the practitioner is authorized to file correspondence in the above-identified application pursuant to 37 CFR 1.34, including to prosecute this application, to make alterations and amendments therein, to receive the patent, and to transact all business in the United States Patent & Trademark Office connected therewith.

The practitioner named above does not have the authority to sign a request to change the correspondence address, a request for an express abandonment, a disclaimer, a power of attorney, or other document requiring the signature of the applicant, assignee of the entire interest or an attorney of record. If appropriate a separate Power of Attorney to the above named practitioner should be executed and filed in the United States Patent and Trademark Office.

Respectfully submitted,



Richard J. Streit, Reg. 25765
Ladas & Parry
224 South Michigan Avenue
Chicago, Illinois 60604
(312) 427-1300

Dated: March 8, 2005